2009 Investor Meeting



Anand Chandrasekher Senior Vice President, Intel Ultra Mobility Group



Consumer Trends are Driving Full Internet and Rich Content

Intel Innovation Continues on Low Power, Packaging and Cost

TAM Growth Opportunity Across Several Segments





Changing Consumer Tastes

EBSCO Advertising Age		Alexa Global Internet Traffic Rankin				
1999			2005 ¹			2
RankWeb Site1AOL2yahoo.com3Microsoft/msn.com4lycos.com5go.com6realnetworks.com7excite@home8ebay.com9altavista.com		Rank 1 2 3 4 5 6 7 8 9	Web Site yahoo.com msn.com google.com ebay.com amazon.com microsoft.com myspace.com google.co.uk aol.com		Rank 1 2 3 4 5 6 7 8 9	
10 timewarner.com		10	go.com		10	

Rapidly Changing Mix, Unpredictable Leaders

Traffic rank is based on three months of aggregated historical traffic data from Alexa Toolbar users and is a combined measure of page views / users (geometric mean of the two quantities a veraged over time). (1) Rankings as of 12/31/05, excludes Microsoft Passport; (2) Rankings as of 5/5/09 Source: Alexa Global Traffic Rankings, Morgan Stanley Research

ngs 2009²

Web Site google.com yahoo.com youtube.com facebook.com facebook.com live.com msn.com wikipedia.org blogger.com baidu.com



Today's Mobile Internet Experience Lacking

Top 5 Device Improvements: Smartphone Users

I would use the mobile internet on my mobile phone more frequently if...



Source : Intel Primary Research, Online Survey, U.S. China, Germany, n = 788, Q4 '08





Market Imperatives

LOW POWER FOR ALL-DAY BATTERY LIFE

ALWAYS CONNECTED

PERFORMANCE TO HANDLE RICH CONTENT

GREAT INTERNET AND SOFTWARE EXPERIENCE





Today's Industry Requirements for Smartphone

Metric	Requirements	Bene
Thermal Limits	1.5-2W TDP	Small Forr
Scenario Power	300m₩	AP Effic
Battery Life	8 hours	All-Day
Always Connected	Broadband Wireless, Voice	Data/Voic
Performance	>3000 DMIPS	App. and Medi
Compatibility	Lowest Development Costs Binary Compatible Code Re-use	Multi Pla Compat Full Int

- efit
- rm Factor
- iciency
- , Usage
- ce Access
- lia Processing
- latform atibility ternet



Low Power Roadmap



Continued Power Reduction with 32nm

Additional Target Segment: Smartphones



Continued Progress on Menlow



Intel's First Low Power Architecture Built Ground Up on 45nm Process Technology

> 818183181818 Intel[®] Atom[™] **Intel System Controller Hub** processor

Menlow Going Strong 70+ Design Wins **New Designs Shipping**

Menlow Refresh Launched in April New 1.2GHz and 2.0GHz SKUs Intel[®] Burst Performance Technology





Performance Leadership **Comparing Cores**

Peak DMIPs

Intel Atom 2.0GHz w/ HT 2.6-5.4X Intel Atom 1.6GHz w/ HT Intel Atom 1.2GHz w/ HT Snapdragon 1 GHz 2.6-5.4X Cortex A-B 800MHz Cortex A-B 600 MHz

SPECint_rate2000 Benchmarks (Estimated Integer Performance)

Intel Atom 2.0 GHz with HT Intel Atom 1.6GHz with HT Intel Atom 1.2GHz with HT Snapdragon 1 GHZ

Cortex A-B 800MHz

Cortex A-B 600 MHz

12000 0 4000 8000

>2x the Performance - Across the Range

• Performance tests and ratings are measured using specific computer systems and/or components and reflect the approximate performance of Intel & competitive products as measured by those tests. Results have been simulated and are provided for informational purposes only. Any difference in system hardware or software design or configuration may affect actual performance. Buyers should consult other sources of information to evaluate the performance of systems or components they are considering purchasing. For more Information on performance tests and on the performance of Intel products, visit Intel Performance Benchmark Limitations, Assumptions: (1) ARM11 = 1.2 DMIPS/MHz and Contex A8 = 2.0 DMIPS/MHz (published data- no IPO) (2) ARM and third party datasuggests 5% Improvement for RealView Development Sulte over GCC

Intel Results estimated on Snapdragon 1 GHz

Demo







IA Internet Compatibility

Runs ONLY on IA





MTV*





Hulu*





Runs BETTER on IA



Yahoo!*



Mozilla*

NOTE: Availability of these features and capabilities is dependent on OEM implementations. *Other brand and names are the property of their respective owners.





CSS -Cascading Style Sheets





Moorestown – 2nd Generation LPIA

CPU Core

Integrated CPU, Gfx and Memory

Chipset Integrated Audio, Camera, and Security Functions



- Cutting Edge 45nm Process
 - Hafnium based High-k + metal gate transistors
 - High performance & low leakage power
- Brand New Architecture
 - CPU core, Graphics / Display / Memory controllers on a single die
 - Multimedia accelerators offer rich CE features
- Advanced Power Management
 - Enhanced Intel[®] SpeedStep[®] Technology
 - Deeper sleep state than C6
 - On die power gating

Optimized Architecture To Deliver Dramatic Power Reductions









Moorestown Power Reduction



Standby

Optimized Platform Architecture

Low Power Interfaces

Distributed Power Management

Up to 50X Improvement in Moorestown Idle Power Over Menlow



Standby: Assuming 5013, 38mW with 5011

MRST Projection: Workbads were emulated on McCaslin (GreenCanyon CRB-XP-600MHz-DTN-512MB memory-32GBSSD) and cross checked on some data points with Menlow (Crownbeach CRB, SLT-1GHz, 512MB memory, 32GB SSD). CPU, Memory and CS power data was analyzed with C-state, P-state residencies, Memory Bandwidth and power data points were studied and scaled where appropriate to MRST architecture.. Assuming Native MIPI display and LPDDR1 memory (32b, 400MTs)



Always Connected



3G – Collaboration with Ericsson & OPTION :

- Enables an integrated and optimized 3G solution
- Robust 3G telephony software solution in Moblin
- Reduces design time, power consumption & integration complexities



WIMAX – Multi-megabit mobile broadband with Evans Peak

- World's most highly integrated WiMAX solution: WiFi, BT & GPS
- Moblin 2.0 compliant solution
- Reduces design time, form factor and integration complexities





Medfield – 3rd Generation Handhelds on Intel 32nm Technology Process Leadership Enabling Further Innovations



Single Chip Design on Dedicated 32nm SOC Process Substantial Size and Power Reductions Close Collaboration with Industry Leaders to Define Market-ready Features









Power and Form Factor Reductions On Track Moorestown Idle Is Similar To Phone Level Power

2011

3737 321345 61008

04

G H ROEGLIN

Medfield **Board Size - Reduced** Standby Power - Lower



Meeting Industry Needs – Atom[™] Scorecard

Metric	Requirements	Bene
Thermal Limits	1.5-2W TDP	Small Forr
Scenario Power	300mW	AP Effic
Battery Life	8 hours	All-Day
Always Connected	Broadband Wireless, Voice	Data/Voic
Performance	>3000 DMIPS	App. and Medi
Compatibility	Lowest Development Costs Binary Compatible Code re-use	Multi Pla Compat Full Inte





m Factor

iciency

Usage

e Access

lia Processing

latform tibility ternet



Meeting Industry Needs – Atom[™] Scorecard

Metric	Requirements	Bene
Thermal Limits	1.5-2W TDP	Small Forn
Scenario Power	300mW	AP Effic
Battery Life	8 hours 🖌	All-Day
Always Connected	Broadband Wireless, Voic	Data/Voice
Performance	>3000 DMIPS	App. and Media
Compatibility	Lowest Development Costs Binary Compatible Code re-use	Multi Pla Compat Full Inte





m Factor

iciency

Usage

e Access

ia Processing

latform tibility ternet



Meeting Industry Needs – Atom[™] Scorecard

Metric	Requirements	Bene
Thermal Limits	1.5-2W TDP	Small Forn
Scenario Power	300mW	AP Effic
Battery Life	8 hours 🗸	All-Day
Always Connected	Broadband Wireless, Voic	Data/Voice
Performance	>3000 DMIPS	App. and Media
Compatibility	Lowest Development Costs Binary Compatible Code re-use	Multi Pla Compat Full Inte





m Factor

iciency

Usage

e Access

ia Processing

latform tibility ternet



Our Opportunity in Handhelds



Lower Power

•From Intel Roadmap. Based on Smart Phones TAM with >\$200 ASP, ** Based on Portable Navigation devices TAM, *** Based on Portable Video and Gaming TAM, •**** Based on Tablets, Vertical HHs, and Select Netbooks Data Sources: iSuppli, ABI, IDC, Canalys, Intel analysis

Mainstream





Intel Innovating on Low Power, Packaging and Cost

Investment Amortized Across Multiple Segments

Mobile Internet Devices = Significant Opportunity

GOAL: TAM Expansion at Company Average Margins









Risk Factors

The above statements and any others in this presentation that refer to plans and expectations for the second quarter, the year and the future are forward-looking statements that involve a number of risks and uncertainties. Many factors could affect Intel's actual results, and variances from Intel's current expectations regarding such factors could cause actual results to differ materially from those expressed in these forward-looking statements. Intel presently considers the following to be the important factors that could cause actual results to differ materially from the corporation's expectations. Current uncertainty in global economic conditions pose a risk to the overall economy as consumers and businesses may defer purchases in response to tighter credit and negative financial news, which could negatively affect product demand and other related matters. Consequently, demand could be different from Intel's expectations due to factors including changes in business and economic conditions, including conditions in the credit market that could affect consumer confidence; customer acceptance of Intel's and competitors' products; changes in customer order patterns including order cancellations; and changes in the level of inventory at customers. Intel's results could be affected by adverse effects associated with product defects and errata (deviations from published specifications), and by litigation or regulatory matters involving intellectual property, stockholder, consumer, antitrust and other issues, such as the litigation and regulatory matters described in Intel's SEC reports. Intel operates in intensely competitive industries that are characterized by a high percentage of costs that are fixed or difficult to reduce in the short term and product demand that is highly variable and difficult to forecast. Revenue and the gross margin percentage are affected by the timing of new Intel product introductions and the demand for and market acceptance of Intel's products; actions taken by litel's competitors, including product offerings and introductions, marketing programs and pricing pressures and Intel's response to such actions; and Intel's ability to respond quickly to technological developments and to incorporate new features into its products. The gross margin percentage could vary significantly from expectations based on changes in revenue levels; capacity utilization; start-up costs; excess or obsolete inventory; product mix and pricing; variations in inventory valuation, including variations related to the timing of qualifying products for sale; manufacturing yields; changes in unit costs; impairments of long-lived assets, including manufacturing, assembly/test and intangible assets; and the timing and execution of the manufacturing ramp and associated costs. Expenses, particularly certain marketing and compensation expenses, as well as restructuring and asset impairment charges, vary depending on the level of demand for Intel's products and the level of revenue and profits. The tax rate expectation is based on current tax law and current expected income. The tax rate may be affected by the jurisdictions in which profits are determined to be earned and taxed; changes in the estimates of credits, benefits and deductions; the resolution of issues arising from tax audits with various tax authorities , including payment of interest and penalties; and the ability to realize deferred tax assets. The recent financial crisis affecting the banking system and financial markets and the going concern threats to investment banks and other financial institutions have resulted in a tightening in the credit markets, a reduced level of liquidity in many financial markets, and extreme volatility in fixed income, credit and equity markets. There could be a number of follow-on effects from the credit crisis on Intel's business, including insolvency of key suppliers resulting in product delays; inability of customers to obtain credit to finance purchases of our products and/or customer insolvencies; counterparty failures negatively impacting our treasury operations; increased expense or inability to obtain short-term financing of Intel's operations from the issuance of commercial paper; and increased impairments from the inability of investee companies to obtain financing. Gains or losses from equity securities and interest and other could also vary from expectations depending on gains or losses realized on the sale or exchange of securities; gains or losses from equity method investments; impairment charges related to debt securities as well as equity and other investments; interest rates; cash balances; and changes in fair value of derivative instruments. The current volatility in the financial markets and overall economic uncertainty increases the risk that the actual amounts realized in the future on our debt and equity investments will differ significantly from the fair values currently assigned to them. The majority of our non-marketable equity investment portfolio balance is concentrated in companies in the flash memory market segment, and declines in this market segment or changes in management's plans with respect to our investments in this market segment could result in significant impairment charges, impacting restructuring charges as well as gains /losses on equity investments and interest and other. Intel's results could be impacted by adverse economic, social, political and physical/infrastructure conditions in countries where lintel, its customers or its suppliers operate, including military conflict and other security risks, natural disasters, infrastructure disruptions, health concerns and fluctuations in currency exchange rates. A detailed discussion of these and other risk factors that could affect Intel's results is included in Intel's SEC filings, including the report on Form 10-Q for the quarter ended March 28, 2009.



